### Revised Structure of B. E (Information Technology) Part – I & Part –II  w.e.f. July 2010

#### Part – I

<table>
<thead>
<tr>
<th>Sr. No</th>
<th>Name Of the Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Paper</th>
<th>T/W</th>
<th>OE</th>
<th>POE</th>
<th>Total</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Advanced Computer Architecture</td>
<td>4</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>100</td>
</tr>
<tr>
<td>2</td>
<td>Management Information System</td>
<td>4</td>
<td>-</td>
<td>2</td>
<td>100</td>
<td>25</td>
<td>-</td>
<td>-</td>
<td>125</td>
</tr>
<tr>
<td>3</td>
<td>Advanced Data Base System</td>
<td>4</td>
<td>-</td>
<td>2</td>
<td>100</td>
<td>25</td>
<td>-</td>
<td>50</td>
<td>175</td>
</tr>
<tr>
<td>4</td>
<td>Elective - I</td>
<td>4</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>100</td>
</tr>
<tr>
<td>5</td>
<td>VC ++</td>
<td>2</td>
<td>-</td>
<td>4</td>
<td>-</td>
<td>25</td>
<td>-</td>
<td>-</td>
<td>75</td>
</tr>
<tr>
<td>6</td>
<td>Project - I</td>
<td>-</td>
<td>-</td>
<td>4</td>
<td>-</td>
<td>25</td>
<td>75</td>
<td>-</td>
<td>100</td>
</tr>
<tr>
<td>7</td>
<td>Industry Institute Interaction</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>25</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>25</td>
</tr>
<tr>
<td></td>
<td>Total</td>
<td>18</td>
<td>12</td>
<td>400</td>
<td>125</td>
<td>75</td>
<td>-</td>
<td>100</td>
<td>700</td>
</tr>
</tbody>
</table>

#### Elective – I

1. Artificial Neural Network
2. Object Oriented Modeling & Design
3. Microcontroller
4. Digital Signal Processing
5. Human Computer Interface

#### Part – II

<table>
<thead>
<tr>
<th>Sr. No</th>
<th>Name Of the Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Paper</th>
<th>T/W</th>
<th>OE</th>
<th>POE</th>
<th>Total</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Information Retrieval</td>
<td>3</td>
<td>-</td>
<td>2</td>
<td>100</td>
<td>25</td>
<td>-</td>
<td>50</td>
<td>175</td>
</tr>
<tr>
<td>2</td>
<td>Mobile Computing</td>
<td>4</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>100</td>
</tr>
<tr>
<td>3</td>
<td>Network Security</td>
<td>4</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>100</td>
</tr>
<tr>
<td>4</td>
<td>Elective - II</td>
<td>4</td>
<td>-</td>
<td>-</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>100</td>
</tr>
<tr>
<td>5</td>
<td>Web Technology</td>
<td>3</td>
<td>-</td>
<td>4</td>
<td>-</td>
<td>25</td>
<td>-</td>
<td>50</td>
<td>75</td>
</tr>
<tr>
<td>6</td>
<td>Project - II</td>
<td>-</td>
<td>-</td>
<td>6</td>
<td>-</td>
<td>50</td>
<td>-</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td></td>
<td>Total</td>
<td>18</td>
<td>12</td>
<td>400</td>
<td>100</td>
<td>-</td>
<td>-</td>
<td>200</td>
<td>700</td>
</tr>
</tbody>
</table>

#### Elective – II

1. Pattern Recognition
2. Software Testing & Quality Assurance
3. Embedded systems
4. Image processing
5. VLSI Technology

#### Note:

1. Minimum strength of the Students for Electives be 15.

2. The batch size for the practical/tutorials be of 15 students. On forming the batches, if the strength of remaining students exceeds 7 students, then a new batch may be formed. For Project the group shall be about 4 students.
B.E. (Information Technology) Part-I

1. Advanced Computer Architecture

Lectures: 4 Hours / week      Theory: 100 marks

Section – I

1. Advanced Pipelining: (7)
   Instruction Level Parallelism: Concepts and Challenges, overcoming data hazards with dynamic scheduling, reducing branch penalties with dynamic hardware prediction.

2. Instruction Level Parallelism: (6)
   Taking advantages of more ILP with multiple issue, compiler support for exploring ILP, Hardware support for extracting more parallelism, studies of ILP.

3. Vector Processing: (7)
   Why Vector processor?, Basic vector architecture, two real world issues: Vector length and stride, effectiveness of compiler vectorization, enhancing vector performance.

Section – II

4. Interconnection Networks: (7)
   Tightly and loosely coupled architectures, cluster computing as an application of loosely coupled architecture, various topologies, Static and dynamic types of networks with examples.

5. Dataflow Architecture: (6)
   Concepts of dataflow computing, static and dynamic architectures, dataflow operators, dataflow language properties, advantages and potential problems.

6. Multiprocessors: (7)
   Introduction, characteristics of application domains centralized shared memory architecture, distributed shared memory architecture, synchronization, models of memory consistency

Text Books:

Reference Books:
2. Advanced Computer Architectures A design space approach – Sima, Fountain, Kacsuk- Pearson
3. Computer Organization and Architecture-An Integrated Approach – Miles Murdocca, Vincent Heuring – Wiley India (For Multiple Choice Questions)

Course Objective:
- To impart good theoretical knowledge of advanced systems that will prepare them to learn all system level subjects that will be useful in developing system level design for optimum performance.
- To make students aware with new technologies used in current architectures to meet the challenges of changing scenario in IT Sector at national & international level.
B.E. (Information Technology) Part-I

2. Management Information Systems

Section - I

1. Information Systems in Global & E-Business
   Information Systems in Global Business
   Global E-Business: How Businesses Use Information Systems

2. Information Systems, Organizations, and Strategy
   Ethical and Social Issues in Information Systems

3. Information Technology Infrastructure
   IT Infrastructure and Emerging Technologies
   Foundations of Business Intelligence: Databases and Information Management

4. Communication in IT
   Telecommunications, the Internet and Wireless Technology
   Securing Information Systems

Section – II

5. Key System Applications for the Digital Age
   Achieving Operational Excellence and Customer Intimacy: Enterprise Applications
   E-Commerce: Digital Markets, Digital Goods

6. Knowledge Management Techniques
   Managing Knowledge
   Enhancing Decision Making

7. Building and Managing Systems
   Building Information Systems
   Project Management: Establishing the Business Value of Systems and Managing Change
   Managing Global Systems

Textbook:

Reference Books:

Practical:
Students should design & develop a MIS for an Institution or Industry, using the principles covered in theory

Course Objectives:
This course acquaints a student with the basic infrastructure, strategy for information systems. It introduces a student with the communication technology required for IT & enables him to build and manage corporate information system.
B.E. (Information Technology) Part-I
3. Advanced Database Systems

Lecture : 4Hrs/Week           Theory : 100 Marks
Practical : 2Hrs/Week           T/W : 25 Marks
POE : 50 Marks

Section I

1. Database Systems architectures (10)
   Centralized & C/S architectures, Server systems, Distributed systems, Distributed databases – Homogeneous & heterogeneous databases, Distributed data storage, Distributed transactions, Commit protocols, Concurrency control in distributed databases, Availability, Distributed query processing, Heterogeneous distributed databases.

2. Parallel Databases (6)
   Integrated, I/O parallelism, Interquery parallelism, Intraquery parallelism, Intraoperation parallelism, Interoperation parallelism, Design of parallel systems.

3. Object Database System (6)
   Overview, Complex Data Types, Structured Types and Inheritance in SQL, Table inheritance, Array and Multisets Types in SQL, Object Identity and Reference Types in SQL, Implementing O-R features, Persistent Programming Languages, Object Oriented versus Object Relational

Section II

4. Data Analysis and Mining (6)
   Decision Support Systems, Spatial databases, Temporal Databases, Data analysis and OLAP, Data Warehousing, Data Mining

5. Query Processing and Optimization: (6)
   Overview of Query Processing, Measures of query cost, Selection operation, join operation, Other operation, Overview of Query optimization, Introduction to Transformation of Relational Expressions, choice of evaluation plans, Materialized views.

6. Advanced Application Development (6)
   Performance Tuning, Performance Benchmark, Standardization, Application Migration

7. Advanced Transaction Processing (4)
   Transaction processing monitors, Transactional Workflows, E-commerce, Main Memory Databases, Real Time Transaction Systems, Long Duration Transactions, Transaction Management in Multidatabases
Text Book:


Reference Book:

Practicals:

Minimum 8 assignments based on above topics.

Course Objective:

Having introduced to databases, it is necessary to have a deeper approach in to databases, advanced techniques and tools. This course covers the state-of-the-art techniques to be made known to the students of final year.
4. Elective I
1. Artificial Neural Networks

Lectures: 4 Hours/week Theory: 100 Marks

Section – I

1) Introduction : 4 hrs.
   Biological neuron, Models of artificial neural networks, neural processing, neural network
   learning rules,

2) Learning & adaptation : 5 hrs.
   Classification Neural learning rules-Hebbian, perceptron, Delta, Widrow Hoof, Winner
   take all outstar learning rule.

3) Perceptron : 4 hrs.
   Discrete perception as a classifier, Decision and discriminant functions, Linearly non separable
   patterns. Perceptron training for two class and multiclass dichotomizer.

4) Multilayer networks : 4 hrs.
   Delta learning rule for multiperceptron layer, Generalized Delta learning rule, Feed
   forward recall and error back-propagation, Training algorithm.

5) Performance : 4 hrs.
   Madeline, Network pruning, Marchands, Neural tree and filing algorithm, Prediction network.

Section – II

6) Unsupervised learning : 5 hrs.
   Winner take all networks, Hamming networks, Max net, competitive learning K-means
   clustering and LVQ algorithms, Adaptive resonance theory, ARTI,
   ALGORITHM, SELF ORGANIZING Kohanens map, Naocognitron.

7) Associative memories : 5 hrs.
   Non iterative procedures for association hop field networks, Discrete Hop field Networks
   storage capacity of Hop field networks. Continuous Hop field networks. Brain state in a box
   (B B networks Boltzmann machines Hetero associations.

8) Optimization techniques: 5 hrs.
   Optimization using Hop field networks. Traveling salesperson problem, Iterated gradient
   descent techniques. Simulated annealing technique, Random search technique genetic
   algorithm for optimization problems.

9) Application of ANN : 4 hrs.
   Character recognition, Speech recognition, Signature verification application, Human face
   recognition.
Text Books :
1. Introduction to Artificial Neural Systems – Zurada (JAICO)
2. Elements of Artificial Neural Networks – Mehrotra, Hohan, Ranka (PENRAM)
3. Introduction to Artificial Neural Networks – B. Yegnanarayana (PHI)

Ref. Books :
1. An introduction to ANN by Anderson (PHI)
2. Neural Networks a comprehensive foundation by Haykin (PHI)
3. Elements of ANN by Mohan Ranka (Pearam International)

Course Objectives:
This preliminary course of Artificial Neural Network enables a student to understand basic elements of Artificial Neural Network and make him use the same to develop higher level strategies for the betterment of the output established through this network. It helps a student to design and develop real world problems using neural computations.
B.E. (INFORMATION TECHNOLOGY) PART – I
4. Elective I
2. Object Oriented Modeling And Design

Lectures: 4 hrs/week                         Theory: 100 Marks

Section-I

1. Introduction:                           4 hrs
Object Oriented development and themes, evidence for usefulness, modeling as a Design Technique.

2. Object Modeling:                       6 hrs
Objects, classes, links and associations, generalization and inheritance, grouping constructs, aggregation, abstract classes, generalization as extension and restriction, multiple inheritance, metadata, candidate keys and inheritance.

3. Dynamic and Functional Modeling:       6 hrs
Events, states, operations, concurrency, nested state diagrams, advanced dynamic modeling concepts, relation of object and dynamic models, DFD, relation of functional to object and dynamic models

4. Implementation of OMT                 6 hrs
Use of programming language and database system, Object oriented style, feature of object-oriented languages, Applications of OMT like object diagram compiler, Computer animation

Section-II

5. Structural Modeling using UML:         6 hrs
Classes, Relationships, Common mechanisms. Diagrams, Class Diagrams, Interfaces, Types and Roles, Packages, Instances and Object Diagram

Interactions, Use cases, Use case diagram, Interaction Diagrams and Activity diagrams, Events and signals, State Machines, Processes and Threads, Time and space, State chart diagrams.

7. Architectural Modeling using UML:      6 hrs
Components, Deployment, Collaboration, Patterns and Frame works, Component diagrams and Deployment Diagrams

Text Books:
1. Object oriented Modeling and Design:   Rambaugh, Premerlani, Eddy, Lorenson (PHI )

Reference:
Practical Object Oriented Design with UML – Mark Priestley.
  1. UML-In a Nut Shell – Sinon Alhair

Course Objectives: To introduce a student with two base methodologies of modeling design in an object oriented scenario namely Object Modeling Technique and Unified modeling language. It enables a student to model and design real world problems.
B.E. (INFORMATION TECHNOLOGY) PART – I
4. Elective I
3. Microcontroller

Lectures 4 Hrs/Week                     Theory: 100 Marks

Section I
1. MCS 51 Microcontroller family
   a. Introduction to MCS 51 family, Architecture, Memory organization, Functional pin description, SFRs and various resources of MCS 51. (4)
   b. Addressing modes, Instruction set and c programming. (4)
   c. Hardware Overview
   Study of Interrupt structure, Port structure Operation, Timer 0, Timer 1, Timer 2, Serial port and multiprocessor communication. (5)
   d. Interfacing of External data. Program memory and peripherals (ADC0809, DAC 0808 etc) for I/O expansion. (4)
   e. Programming MCS 51 and Study of assemblers and Simulators for MCS 51 Family (MCS 51 IDE from Kail or other similar manufacturer) (3)

Section II
2. Microchip PIC Microcontrollers
   a. Introduction to Microchip PIC 16Fxx family of Microcontrollers (2)
   b. CPU architecture and instruction set, Harvard Architecture and Pipelining, (5)
   c. Program memory considerations, Register file structure and addressing modes, CPU Registers (3)
   d. I/O Ports and TRIS registers (2)

3. AVR Microcontroller family
   a. The AVR RISC microcontroller architecture (3)
   b. The AVR instruction set, AVR Hardware design issues (3)
   c. Hardware and software interfacing with AVR. (2)

Books:
1. Intel or Atmel MCS 51, PIC 16fxx, AVR Family Microcontrollers Data Sheets.
2. The 8051 Microcontroller and Embedded Systems Using Assembly and C, 2/e By Muhammad Ali Mazidi, Pearson Education Asia LPE
5. Design with PIC Microcontrollers By John B. Peatman, Pearson Education Asia. LPE

Course Objectives: This course is a first level course on Microcontrollers. It introduces the architecture and interfacing details of microcontrollers and interfacing details of microcontrollers and enables a student to design applications using microcontrollers.
B.E. (Information Technology) Part-I
4. Elective I
4. Digital Signal Processing

Lectures: 4 Hrs/ Week  Theory: 100 Marks

SECTION - I

1: DISCRETE TIME SIGNALS & SYSTEM: 6 Hrs
Discrete time signal: sequences; discrete time system, classification; linear time invariant systems, its properties; frequency domain representation of discrete time signals and systems; symmetry properties of the Fourier transform, Fourier transform theorems.

2: Z TRANSFORM: 5 Hrs
Definition and properties, the region of convergence; bilateral Z transform, inverse Z transform; Z transform properties.

3: DISCRETE FOURIER TRANSFORM: 8 Hrs
Representation of periodic sequence: the discrete Fourier series- properties; sampling in time and frequency domain; fourier representation of finite duration sequences; the discrete fourier transform-properties; linear convolution using the DFT; 2 dimensional DFT; discrete time fourier transform.

4: REALISATION OF DIGITAL LINEAR SYSTEMS: 5 Hrs
Introduction, basic realization, block diagram & the signal flow graph; basic structures for IIR & FIR systems.

SECTION - II

5: DIGITAL FILTER DESIGN TECHNIQUES: 7 Hrs
Design of IIR digital filters from analogue filters; properties of FIR digital filters; Design of FIR filters using windows; comparison of IIR and FIR filters, linear phase filters.

6: COMPUTATION OF THE DISCRETE FOURIER TRANSFORMS: 7 Hrs
Decimation in time algorithms; decimation in frequency Algorithms; FFT algorithms for an N composite number; General computational Considerations in FFT algorithms; chirps Z transform algorithm.

7: DISCRETE HILBERT TRANSFORM: 5 Hrs
Real and imaginary part sufficiency for casual sequences; minimum phase condition; Hilbert transform relations for the DFT and the complex sequences.

8: DSP PROCESSORS 3 Hrs
TMS 320 Architecture, Applications

BOOKS:
TEXT BOOKS:
1: Oppenheim Schaffer,” DISCRETE TIME SIGNAL PROCESSING” (PHI),2001
2: Proakis J.G ,” INTRODUCTION TO DIGITAL SIGNAL PROCESSING” (PHI),1997
Course Objectives: To introduce a student to analyses Digitals in time and frequency domains and to use. Digital Signal Processors to create applications where signal transformations are required.
Lectures: 4 Hours/week

Theory: 100 Marks

**Section I**

1. **Human Computer Interface**: Introduction, Brief History (4)
2. **User Interface Design**: Models, Principles, Practices (4)
3. **Direct Manipulation**: Overview, Scope, Application (4)

**Section II**

5. **User Modelling**: Interaction with Natural Languages, Next Generation Interface (4)
   - Heuristic Evaluation
   - Evaluation with Cognitive Models
   - Evaluation with Users
   - Model-based Evaluation

Textbooks:


**Course Objectives**: To introduce a student to human computer interfaces, their designs, applications, cognitive framework and to use them in modeling and interaction with natural languages to develop next generation interfaces for real world problems.
5. Visual C++

1. Introduction to Windows Operating system. Developing window application in SDK. Detail study of Windows messages.

2. Introduction to GDI – understanding DC, scrollbars, drawing lines, dots, GDI mapping modes, drawing filled areas.

3. Working with keyboard & mouse – understanding keyboard & mouse basics, keyboard messages, mouse messages, capturing mouse

4. Working with Window controls – study of various buttons, study of controls, edit box, scrollbars list box etc.

5. Working with menus – adding icons, cursor, custom resources, adding menus to application, enabling disabling menu items.

6. Working with Dialog boxes – modal dialog box, modeless dialog box, common dialog box

7. Working with clipboard – standard data formats, memory allocation, transferring data to the clipboard, getting data from clipboard.

8. Developing Multiple Document Interface (MDI) & Dynamic Linked Libraries (DLL)

9. Introduction to MFC. Creating SDI application.

10. Implementation of View and Documents.
    Defining and initializing view class data members, Storing and deleting the document data, scrolling and splitting views.

11. Study of Documents & Views
    Understanding document template, using documents & views together

Text Books :
1. Programming Windows fifth edition by Charles Petzold Microsoft press
2. Mastering Visual C++ 6.0 –Michael J. Young – Techmedia

Reference Books :
1. Programming Visual C++ by david Kruglinski,shepherd,Wingo Microsoft press
2. Complete Reference VC++ 6 – Pappas Murray (TMGH).
3. MFC Programming from the ground up by Herbert Schildt (TMH )
Objective:
There are two objective of the course
1. It helps to focus on totally new and different software development ie. Windows GUI Programming.
2. To get acquainted with core working of Window Message Processing and Window API

List of Practical

1. Study of Different Windows Operating System
2. Write a skeleton program for creating any application window.
3. Study and Implementation of various GDI functions.
4. Implementation of Keyboard and mouse messages
5. Implementation of Shopping cart using child window controls.
6. Study and implementation of resources : cursors, icon and string table
7. Implementation of menus and dialog boxes and implement its messages.
8. Create a DLL for matrix addition , multiplication and subtraction.
9. Create a window using MFC
10. Create a MDI application.
1. A project group shall be about 4 to 5 students.

2. Students have to study existing system, problems in existing system, proposed system, its
definition, scope, design, introduction to programming tools, hardware and software
platforms, planning, activity charts, planning for testing, test case design etc.

3. Project leader should maintain the progress register in which each member's weekly
contribution should be written and the guide will countersign the same.

4. A project design report will be submitted as a term work at the end of semester.
B.E. (INFORMATION TECHNOLOGY) PART – I

7. Industry Institute Interaction

Term Work : 25

The student should attend an industrial training arranged at Industry or Institute and should complete a mini project on the technology on which training was given. A report regarding satisfactory completion of the training should be submitted to the college by competent authority from Industry / Institute. The evaluation of Term Work will be carried out by a panel of Examiners decided by the institute.
1. Information Retrieval

Lectures: 3 Hrs/week       Theory : 100 Marks
Practical : 4 Hrs /Week       POE : 50 Marks.
T/W : 25 Marks

Section – I

1. Information Retrieval & IR Models :

2. Query Languages:
Keyword based querying, Pattern Matching, Structural Queries.

3. Text and Multimedia Languages and Properties :
Text data & formats, Multimedia Data & formats.

4. Indexing and Searching :
Inverted Files and Indices for text search, Boolean Queries, Sequential searching, Pattern Matching, Structural Queries.

Section – II

5. Multimedia IR - Models and Languages :
Data Modeling & Query Languages.

6. Multimedia IR - Indexing and Searching :
A generic multimedia indexing approaches, One dimensional time series, Two Dimensional color images, Automatic Feature Extraction.

7. Searching the Web:
Search Engines ,Browsing, Metasearchers, Searching using Hyperlinks

8. Digital Libraries :

Text Book -

Reference :
1 www.dcc.ufmg.br/irbook or sunsite.dcc.uchile.cl/irbook
3 Information Storage and Retrieval- Robert R Korthage, WILEY-INDIA

Course Objectives : This course introduces students with the principles of information retrieval from text, multimedia and web . During the course students has to undergo different mathematical models and algorithms for the same.
B.E. (INFORMATION TECHNOLOGY) PART – II
2. Mobile Computing

Lectures: 4 Hrs/Week Theory: 100 Marks

Section – I

1. Introduction to wireless communication: (2)
   Introduction, Need and Applications of wireless communication, Mobile and wireless devices.

2. Wireless transmission: (5)
   Frequencies for radio transmission, signals, antennas, signal propagation, Multiplexing, Modulation, Spread spectrum and Cellular systems.

3. Medium Access Control: (5)
   Specialized MAC, SDMA, FDMA, TDMA and CDMA.

4. Telecommunication Systems: (10)
   GSM– Mobile services, System architecture, Radio interface, Protocols, Localization and calling, Handover, Security, New data services

Section – II

5. Wireless LAN: (10)
   Introduction, Infrared v/s Radio transmission, Infrastructure and ad-hoc networks, IEEE 802.11, HIPERLAN, Blue Tooth.

6. Mobile Network Layer: (5)
   Mobile IP, DHCP.

7. Mobile Transport Layer: (6)
   Traditional TCP, Indirect TCP, Snooping TCP, Mobile TCP, Fast and selective retransmission & recovery, Transaction oriented TCP.

Books:

Term Work: It should consist of 8-10 assignments on above topics with emphasis on designing & solving problems on above mentioned topics.

Course Objective of Mobile Computing

- To give good theoretical background that will prepare them to learn the state-of-the-art technology of mobile communication that will be useful in developing a successful professional career.
- To introduce students with new technology used in Mobile Communication to meet the challenges of changing scenario in IT Sector at national & international level
B.E. (INFORMATION TECHNOLOGY) PART – II
3. Network security

Lectures: 4 Hrs/Week                        Theory:   100 Marks

Section – I

1) Symmetric Ciphers: 6 hrs.
Overview – Services, Mechanism and Attacks, the SI Security. Architecture, A model for
network security Classical Encryption techniques – Symmetric Cipher model, Substitution.
Techniques, Transposition techniques, Rotor Machines, Steganography.

2) Block Cipher and Data Encryption Standard: 6 hrs.
Simplified DES, Block Cipher principles, The Data Encryption Standard, The strength
of DES, Differential and Linear Cryptanalysis, Block Cipher design principles, Block Cipher
mode of Operation.

3) Public Key Cryptography: 5 hrs.
Public Key Cryptography and RSA – Principles of Public Key Cryptosystems, The RSA
Algorithm Key management; other public key cryptosystems – Key Management,
Diffie-Hellman Key Exchange, Elliptical Curve Arithmetic, Elliptical curve Cryptography

4) Message Authentication and HASH Functions: 6 hrs.
Authentication requirements, Authentication Functions, Message Authentication Codes,
Hash Functions, security of Hash Functions and MACS Digital Signatures and
Authentication Protocols – Digital Signatures, Authentication Protocols, Digital Signature
Standard.

Section – II

Authentication Applications – Kerberos, X.500 Authentication Service Electronic Mail
Security – Pretty Good Privacy, S/MIME

IP Security Overview, IP Security Architecture, Authentication Header, Encapsulating
security payload, Combining Security Associations, Key Management, Secure Socket Layer

Privacy on internet, Privacy consideration in web services, Privacy in semantic Web, Privacy
consideration in the use of Context-Sensitive Technologies, Security and privacy aspect of
service oriented architectures.

8) System Security: 4 hrs.
Intruders – Intruders, Intruder detection, Password Management, Malicious Software – Viruses
and Related Threats, Virus Countermeasures, Firewall design principles, Trusted system.
Text Book:

1. Williams Stallings–Cryptography and Network security principles and practices. Pearson Education (LPE)
2. Nina Godbole --Information systems security-Security management, metrics, frameworks and best practices(WILEY)

Reference Books:


Course Objectives:
This preliminary course of Network security enables a student to understand basic elements of security in the network, internet and web technology. Keeping data, information and knowledge secure from intruders and competitors. To achieve this understanding information and communication systems from a security viewpoint. It has provided aspects of information systems, their corresponding security risks and how to embark on a strategic approach to reducing and preferably, eliminating those risks. This is relevant to industry practice helps a student to design and develop real world problems.
1) Pattern Recognition

Lectures : 4 Hrs/week  Theory : 100 Marks

Section - I

1. Introduction : (6)

2. Decision Functions : (6)
Introduction, Linear Decision Functions, Generalized Decision Functions, Pattern Space and Weight Space, Geometrical Properties, Implementation of Decision Functions, Functions of several variables.

3. Pattern Classification by Distance Functions : (6)
Introduction, Minimum-Distance Pattern Classification, Cluster Seeking, Unsupervised Pattern Recognition.

4. Pattern Classification by Likelihood Functions : (6)
Introduction, Pattern Classification as a Statistical Decision Problem, Bayes Classifier for Normal Patterns, Error Probabilities, A Family of Important Probability Density Functions, Estimation of Probability Density Functions.

Section - II

5. Trainable Pattern Classifiers - The Deterministic Approach : (6)

6. Trainable Pattern Classifiers - The Statistical Approach : (6)

7. Pattern Preprocessing and Feature Selection : (6)

8. Syntactic Pattern Recognition : (6)
Introduction, Concepts From Formal Language Theory, Formulation of the Syntactic Pattern Recognition Problem, Syntactic Pattern Description, Recognition Grammars, Statistical Considerations, Learning and Grammatical inference, Automata as Pattern Recognizers.
Text Book: Pattern Recognition Principles by Julius T. Tou, Rafael C. Gonzalez
(Addison Wesley Publishing Company)

Ref. Book:
1. Pattern Recognition & Image Analysis by Earl Gose & Richard Johnson Baugh
   Steve Jost (PHI)
2. Syntactic Pattern Recognition & Applications by K. S. FU (PHI)
3. Pattern Recognition - Statistical Structural & Neural Approaches by Robert Schalkoff (Wiley India Edition)

Course Objectives: This course introduces students with the principles of pattern recognition required for identifying and analyzing patterns from the real world data. It involves techniques for pattern classification, preprocessing feature selection and syntactic pattern recognition.
B.E. (INFORMATION TECHNOLOGY) PART -II
4. Elective-II
2) Software Testing & Quality Assurance

Lectures : 4 Hrs./Week  Theory: 100 Marks

Section-I

1) Basic Concepts and Preliminaries 6 hrs.

2) Software measurement & metrics data collection 6 hrs.
Measurement in software engineering, classifying software measures, applying the framework, software measurement validation. Definition, storing, extraction and collection of data, analyzing software measurement data, analyzing results of experiments, simple analysis techniques, more advanced methods, Overview of statistical test.

Size: software size, length, reuse, functionality, complexity. Structure: Types of structure measure, control flow structure, Modularity and information flow attributes, Object oriented metrics, Data structures.

Section-II

4) Software Testing Techniques: 8hrs

5) Software Reliability 8 hrs.
Definitions of Software Reliability, Factors Influencing Software Reliability, Applications of software Reliability, operational Profiles, Reliability Models, Test Groups, Software Quality Assurance Groups, Effective Staffing of Test Engineers, Recruiting Test Engineers, Retaining Test Engineers, Team Building

6) Software Quality 4 hrs.
(Minimum 8 to 10 Experiments to be conducted on above topics)

Text Books:

1) Software Testing and Quality Assurance: Theory and Practice, Sagar Naik, Piyu Tripathy
2) Software Metrics – A rigorous & practical approach
   - Norman Fenton, Shari Lawrence Pfleeger (THOMSON – BROOKS)

Reference Books:

1) Software Testing – A practical approach, Er.Rajiv Chopra (S.K.KATARIA & SONS)

Course Objectives for Software Testing & Quality Assurance

This course should result in
Understanding the principles of development of software required to build applications.
Demonstrate the features of software used at system level.
Create softwares that are interface between Hardware & User.
B.E.( INFORMATION TECHNOLOGY ) PART -II
4. Elective-II
3. Embedded Systems

Lectures: 4 Hrs / Week Theory: 100 Marks

Objective: Embedded system tools and products are evolving rapidly. This course deals with various approaches to building embedded systems. It introduces unified view of hardware and software. The aim of this course is to make the students aware of the various applications of embedded systems.

Pre-requisites: Microprocessors, Microcontrollers and C Programming

Section – I

1. An overview of embedded systems: 8
   Introduction to embedded systems, Categories and requirements of embedded systems, Challenges and issues related to embedded software development, Hardware/Software co-design, Introduction to IC technology, Introduction to design technology

2. Embedded Software development: 8
   Concepts of concurrency, processes, threads, mutual exclusion and inter-process communication, Models and languages for embedded software, Synchronous approach to embedded system design, Scheduling paradigms, Scheduling algorithms

3. Real Time Operating System: Introduction to RTOS, Basic design using RTOS 4

SECTION-II

4. Parallel Interface Standards: 8
   Various methods of interfacing, Parallel I/O interface, IEEE 1284, ISA, PCI, Parallel Prot interface with keyboard, switches and display units.

5. Serial Interface Standards: IEEE 1394, USB, I2C, SPI, UART. 8

6. Case studies and Applications of embedded systems: 4
   Case Studies of: Digital Camera, Coffee wending machine, Network Router

Text Books:
2. Vahid, ”Embedded System” WILLY INDIA

Reference:
1. K.V.K.Prasad, ”Embedded Real time systems” Willy India
Section – I

1. **Image, digitized image & it’s properties:**
   - Elements of visual perception & its attributes,
   - Digitised Image - image function, mathematical representation.
   - Image digitization - Sampling & Quantization,
   - Properties - distance, pixel adjacency, region, background, holes, brightness, segmentation, border, edge, convex hull, histograms, color, Noise.
   - Image analysis - Level of image data representation Traditional & hierarchal data structure.

2) **Image pre-processing:**
   - Brightness transformation, geometric transformation, Local Processing, Image smoothing and edge detection, Introduction to Image restoration.

3) **Image enhancement in special domain:**
   - Threshold, Edge-based segmentation, Edge relaxation, Border tracing, Hough transform.
   - Region-based segmentation, Region merging, Region splitting, Split & Merge.

Section – II

4) **Image Enhancement in frequency domain:**

5) **Space reorientation and Detection:**
   - Region Identification, Contour-based representation. Chain codes, B-Spline reorientation, Region-based representation, moments, Convex Hull.

6) **Image Compression:**
   - Redundancy & fidelity criteria, Error free compression, Methods of compression, standards, Binary, continuous tone still, Video.

Text Book:
1) **Computer vision & Image processing** - by Milan Sonaka.
2) **Digital Image Processing** - by Gonzalez (Addision Wesley)

Reference:
3) **Elements of Digital Image Processing & Computer Vision** – by Andrew Low(MGH)
4) **Digital Image Processing** - Pratt.
5) **Fundamentals of digital Image Processing** – by A. K. Jain

Objective: This syllabus is designed to explore several image processing techniques, and learn to improve images with them. Extract quantitative data from images.
B.E. (INFORMATION TECHNOLOGY) PART –II
4. Elective-II
5. VLSI

Lectures: 4 Hrs / Week  Theory: 100 Marks

Section – I

1. Digital Design: (6)
Characteristics (Power dissipation, Noise margin, Fan in, Fan out), Single channel MOS inverter, CMOS inverters, CMOS gates, Transmission gates, Delays and loading consideration.

2. Finite State Machines: (8)
Sequential and combinational circuit design, Moore and Mealy machine, Design examples using PLD’s- Barrel shifter, Synchronous controllers, Timing considerations.

3. Architecture of VLSI processors: (9)
CPLD and FPGA, ARM/SPARTAN, The architecture of above in Xilinx and Altera with specifications, block diagram and their comparison. Multiplex & demultiplex keyboard and display interface.

Section-II

4. VHDL: (9)
Introduction, entity, architecture, configuration/behavior, package declaration, data objects, data types, operators, attributes. Statements: process, variable, signal, wait, if-then, when, null, next, exit. Overloading, VHDL code for various sequential, combinational circuit, state machines. Multiplexed and non-multiplexed keyboard and display interface.

5. EDA Tools: (8)
Information on a complete tool from design entry to place and route with optimisation considerations, Information to EDA tools for simulation and synthesis, Design of Test bench.

Term work:
1. Combinational design using structural and behavioral modeling.
2. Sequential design considering clock aspect.
3. Simulation and synthesis of state machine.
4. Downloading of combinational and sequential circuit for CPLD or FPGA.
At least two programs of each above.

Text book:
1. VLSI Design Techniques for analog and digital circuits
   Randall L. Geiger, Phillip E. Allen, Noel R. Strader
2. VHDL primer, J.Bhasker.
4. VHDL Analysis and Modelling of digital systems, Navabi.
5. Xilinx manual

Course Objective: To introduce students with the basic architecture of VLSI processors. To use VHLD to analyze and model digital systems and to use EDA tools for simulation, synthesis and Design of applications.
1. HTML, CSS and XHTML  
   HTML features, syntax, Lists, Links, Tables, Frames, Forms, Colour and images, multimedia, scripts and DHTML, CSS basics, style definitions, CSS values and Units, CSS inheritance and Cascade, layouts, connectivity with database.

2. XML Primer:  
   Introduction, Benefits, components of XML, XML schemas DTD, Parsing XML, Parsing methodologies, X Link, X pointer, X Include, XBase, XML Technologies & applications viz. E-Commerce, XLS: Overview, applications and programming with XLS.

3. JSP  
   JSP overview, JSP language basics, JSP translation and compilation Directives, Standard java objects from JSP, JSP configuration and deployment, Actions and tags of JSP; Java servlets – Arch, servlets interface, applications of Servlets.

4. ASP and ASP.NET:  

ASP.NET  
   Navigation Controls-Sitemap Path, Tree view, Menu. Validation and login controls.

5. Web services:  
   Introduction to web services, service oriented architecture and web services, web services application scenario. Simple object access protocol (SOAP) SOAP introduction, interaction, SOAP modeling SOAP Encoding, SOAP binding.) Web services description language What is WSDL, Web services invocation & WSDL, Web services Description details, Service Description through WSDL.
   Registers: Universal description, Discovery and Integraction, What is UDDI, UDDI nomenclature, care UDDI, Services publication, services discovery.

6. PHP and MySQL  
   Introduction to PHP, variables and constants, program flow, functions, arrays and files and directories, Forms and Databases, integration with Mysql applications on Php.
7. **Ruby on Rails** (6hrs)
Introduction, rails in depth using active record, Controller in depth and view in depth, Developing applications on ruby on rails.

**Practicals:**
10-15 Practicals on each Topics of the above

**Reference & Textbooks:**
2. HTML, XHTML, CSS – Stevan Schafer Wiley india
3. Web Design Technology- D.P. Nagpal, S.Chand
4. Head First Servlets and JSP Bryan Bashan, Kathy Sierra and Bert Bates – O’reilly
5. Web Engineering – Gerti Kappel, Birgit Proll, Siegfried Reich – John Wiley & Sons Ltd
6. Ruby on Rails – Timothy Fisher – Wiley India
7. Php and Mysql - Steve Suehring – Wiley India

**References:**
1. Head First HTML with CSS and XHTML, Elizabeth Freeman and Eric Freeman –O’reilley
2. Head First Servlets and JSP Bryan Bashan, Kathy Sierra and Bert

**Course Objective:** To introduce student with technologies required to build and engineer web sites and to evaluate the performance of websites. These technologies can also we use for developing applications using websites and portals.
1. Project –II should contain the work like Design review, Implementation details, coding, Technologies used, Testing, Task distribution. Project leader should maintain the progress register in which each members weekly contribution should be written and the guide will countersign the same.

2. A project report along with above progress register will be submitted as a term work at the end of semester. Report must include References, Appendix, User manual / Technical reference manual, CD containing Project documentation, implementation, code, Required utilities, Software and Manuals.

3. Every student must prepare well formatted, printed and hard bound report.